Proted on February	12, 2635	A16						PATE	ENT AP	PLICATIO
FORM PTO-1449			ATTY, DOCKET NO.	SIL	.P0061	Page 1 of 10/671,016				
	/ mark	E 17 mm	3	APPLICANT	Du	puis et al.	L			***************************************
1		\$1	HUNGDAH 9 25 2003			CARP 2817				
REFERENC	E DES	CAJENN IGNATION	Ľ.	S. PATENT DOC	UME:	NTS			· - · · ·	
EXAMINES INITIAL		DOCUMEN		DATE		NAME		CLASS		SUB CLASS
			1							/
	$ \leftarrow $									
			\perp							
		-		\longrightarrow	<					
			_	_/_					_	
			+							
	+		+-				$\overline{}$		-	
		1	-						\dashv	
/	-		-		·				\forall	
	1			FOREIGN PAT	ENT	DOCUMENTS				
		DOCUMENT		DATE	\Box	NAME	CLAS	S SUB CLAS	TRANSLATION	
		NUMBER						CDAS	YES	NO
					士					
		OTHER RE	FERE	NCES (including	Autho	or, Title, Date, Pei	tinent Pag	ges, etc.)	•	
H- < ·	-	IBM Whitepaper, "SOI Technology: IBM's Next Advance in Chip Design", http://www.at400.dk/pubfiles/soipaper.pdf, publication datc unkown.								
H.c.		Chew, K.W., "Impact of Deep N-well Implantation on Substrate Noise Coupling and RF Transistor Performance for Systems-on-a-Chip Integration", 32nd European Solid State Device Research Conference, September 24-26, 2002.								

DATE CONSIDERED

choe

Rev. 10/94 (Form 3.05)